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(71) Applicant (*for DE only*): **PHILIPS INTELLECTUAL PROPERTY & STANDARDS GMBH [DE/DE]**; Stein-damm 94, 20099 Hamburg (DE).

(71) Applicant (*for all designated States except DE, US*): **KONINKLIJKE PHILIPS ELECTRONICS N.V. [NL/NL]**; Groenewoudseweg 1, NL-5621 BA Eindhoven (NL).

(72) Inventors; and

(75) Inventors/Applicants (*for US only*): **ROHRMOSER,** Bernd [DE/DE]; Philips Intellectual Property & Standards GmbH, Weisschausstr. 2, 52066 Aachen (DE). WEISS, Thomas [DE/DE]; Philips Intellectual Property & Standards GmbH, Weisschausstr. 2, 52066 Aachen (DE).

(74) Agent: **VOLMER, Georg**; Philips Intellectual Property & Standards GmbH, Weisschausstr. 2, 52066 Aachen (DE).

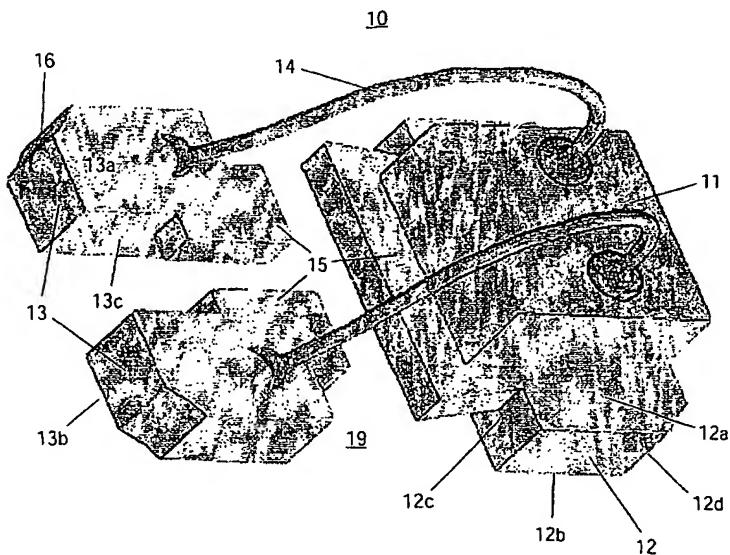
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*[Continued on next page]*

(54) Title: QUAD FLAT NON-LEADED PACKAGE COMPRISING A SEMICONDUCTOR DEVICE

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(57) Abstract: A semiconductor package is proposed. The semiconductor package includes a discrete semiconductor chip on a die pad; a plurality of bond pads situated next to the chip and formed with a plurality of connecting mechanisms and an encapsulant for encapsulating the chip and the pads. In a preferred embodiment the die pad and/or the bond pads comprise means for vertically and laterally interlocking the pads to the encapsulant. The interlocking means of the bond pads and the die pad significantly enhance the bonding strength between the pads and the encapsulant for preventing delamination or cracking, so that quality and reliability of the quad flat non-leaded semiconductor package comprising a discrete can be assured.



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## B. FIELDS SEARCHED

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Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

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EPO-Internal, PAJ, WPI Data

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
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X	US 2002/063257 A1 (WANG BILY) 30 May 2002 (2002-05-30)	1
A	abstract; figures 2,4,5,8 page 1, paragraph 24 claim 1	2-5
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Patent family members are listed in annex.

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## Name and mailing address of the ISA

European Patent Office, P.B. 5818 Patentlaan 2  
NL - 2280 HV Rijswijk  
Tel. (+31-70) 340-2040, Tx. 31 651 epo nl,  
Fax (+31-70) 340-3016

## Authorized officer

Favre, P

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